



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2818
Examiner: NGUYEN, THINH T

In Re PATENT APPLICATION Of:

Applicant : Naofumi IWAMATO

Serial No. : 10/717,655

Filed : November 21, 2003

For : METHOD OF MANUFACTURING)
SEMICONDCUTOR CHIP HAVING)
SUPPORTING MEMBER AND)
SEMICONDCUTOR PACKAGE HAVING))
THE SEMICONDCUTOR CHIP)

Attorney Ref.: F00ED0440-DIV)

) **SUBMISSION OF
FORMAL DRAWINGS**

August 5, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are FOUR (4) replacement sheets of formal drawings (Figures 20-27). These formal drawings include changes, which are being made in conformance with the examiner's requirement, described in the Office Action dated April 23, 2004, to add "PRIOR ART" in these drawings.

Respectfully submitted,


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